A practical approach to reducing development risk and time through Model-driven Development





Het ontwerpen van innovatieve elektronica

Introduction: Who am I

- Jasper Keuning; Lead Electronics Engineer @ 3T since 2012
- Modeling and Simulation
 - Mathworks tooling (Matlab, Simulink, Simscape)
 - Signal & Power Integrity (Ansys)
 - Thermal Analysis (Ansys)
- High speed digital boards
- Medical devices
- Standards and Regulations



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Agenda

- What is Model-driven Development (MDD)
- MDD in the design process
- Project: Skytron Freedom



What is Model-driven development (MDD)

- Using models and simulation to:
 - Create
 - Evaluate
 - Optimize
- It is not a goal, just a means to enhance the design process
- Applicable for many phases of the development cycle



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- Specification Phase:
 - Feasibility





- Specification Phase:
 - Feasibility
 - Identification of critical behavior





- Specification Phase:
 - Feasibility
 - Identification of critical behavior
 - Model as Requirement





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• Product level design:

Software

voltage

limit

velocity (v)

desired

current

 (I_d)

phase

restance

R

phase inductance +

1

 \overline{KV}

estimated

BEMF

voltage

 n_{pp}

• Multidomain modeling and simulation

phase

voltage

ua

voltage

limit

LowPass

Filter

Park+Clarke

Space Vector

phase

voltage

ud

d/dt

• System level evaluation/optimization



- Product level design:
 - Multidomain modeling and simulation
 - System level evaluation/optimization
 - System level verification and integration
 - Multidisciplinary design language







- Module design Software:
 - State diagrams
 - Filters

...

Stateflow (chart) sf_boiler/Bang-Bang Controlle

sf_boiler
Bang-Bang Controller

entry: turn_boiler(OFF)

en: flash_LED()

2. 8

Q Heater

0

r

24

4

4

団

Θ

Ready

Bang-Bang Controller

Off

Flash

after(20,sec)

File Edit View Display Chart Simulation Analysis Code Tools Help

after(5,sec)

after(40,sec)

[cold()]

• Control Algorithms

[Heater.On.warm()]

161%

Normal











The challenge:

- Easy positioning and movement of arm
 - Drift due to mechanical bending
 - Interacting with devices on arm cause movement
 - Accidental Impacts may have large consequences
- Intentional friction in joints
 - Takes quite some effort to move arm around
 - Wear on friction mechanism





The Solution:

- Smart Braking System
 - Easy to move when needed
 - Stable when stationary
 - No special user interaction needed
 - Sensor system to detect user intention







Responsibilities:

- Customer will be responsible for:
 - mechanical design of system
 - user experience
- 3T to develop Electronics and embedded Software

Goal of project: An excellent user experience



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Specification:

• Electronics



Specification:

• Electronics









Specification:

• Software specification



Software specifications (?):

- Ensure all hardware interfaces are accessible and controllable
- The desired user experience of the end-product
- Mechanical design of brakes and arm not available at project start

In other words, a lot still undetermined



Development Strategy: Reduce development time and risk

Use of existing design experience for electronics and software



Development Strategy: Reduce development time and risk

- Use MDD for Functional Behavior design and implementation
 - Common multidisciplinary language for communication
 - Evaluate against model and allow rapid iteration cycles
 - Ability to look deep inside model to gain insight and solve issues
 - Design engineer does not need to be a software engineer



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Development Strategy: Reduce development time and risk

 Functional Behavior will be generated C++ function to be included in C++ project



Customer's Language



Aanpassen Brake Moment:

BRP-mimp: Brake = ALS(IMDM=true; *BRP-brake-max; Brake (last)*) **BRP-stab:** Brake = <u>ALS(STAB=true; BRP-stab; Brake (last)</u>)

Aanpassen Brake Moment:

BRS-mimp: Brake = ALS(IMDM=true; *BRS-brake-max; Brake (last)*) **BRS-stab:** Brake = <u>ALS(</u>STAB=true; <u>BRS-stab</u>; *Brake (last)*)

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	(mg)	(mg)	(mg)	(rad)	(rad)	(rad)	(rad/msec)	(rad/msec)	(rad/msec)		MOVE	MOVE	ROTATE	ROTATE		STABLE	D
2	Ax	Ау	Az	На	Hb	Hc	На	Hb	Нс		intent	active	pendant	carrier		pendant	н
47	3.207	-1.930	0.000	-1.300	-1.981	-1.539	3.728	22.371	. 22.371							JA	
48	3.118	-1.782	0.000	-1.300	-1.981	-1.545	3.728	26.099	22.371							JA	
49	2.970	-1.633	-0.015	-1.300	-1.969	-1.545	0.000	33.556	26.099				JA				Γ
50	3.118	-1.782	0.000	-1.300	-1.969	-1.551	0.000	37.284	29.827				JA				Г











Block Parameters: C1 Force controller PID 1dof (mask) (link) This block implements continuous- and discrete-time PID control algorithms and includes advanced features such as anti-windup, external reset, and signal tracking. You can tune the PID gains automatically using the 'Tune...' button (requires Simulink Control Design). Controller: PD Form: Parallel \sim Time domain: Discrete-time settings PID Controller is inside a conditionally executed subsystem O Continuous-time Sample time (-1 for inherited): Ts 0.01 Discrete-time Compensator formula $P + D \cdot \frac{1}{T_s} \frac{z-1}{z}$ Out (1) ameters C2BrakeVelocity profiled Out seGate Main Data Types Initialization Saturation State Attributes Controller parameters Source: external \sim Use filtered derivative Enable zero-crossing detection D&E EVENT <u>0</u>K Cancel <u>H</u>elp Apply Woensdag 19 april 2023

2

1 Setp

Feedback

3) Congrescentrum 's-Hertoge



Simulate against model of 'real world'





Simulate against specific scenario's

D&CE EVENT

Integration:

- Comparison between model and mechanical system
 - Optimize system and model
- Rapid design iterations to achieve the Excellent user Experience

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Reduced time and risk;

- Design and design-changes represented in common language
- The design has already gone through several evaluation and optimization cycles
- (Major) Changes in high level structure can be made fairly easy without too much pain
- Understanding and solving integration issues without needing access to system
- SW development not longer limited to SW designers

How to reach us

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We're at stand number 2

With an FPGA based MDD demonstration!

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