

Ultra Low Phase Noise Techniques

ALA RIS LINWAVE



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FHI Leusden



Alaris Linwave Technology



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Daniel Lowbridge is a recent employee at Alaris Linwave joining the team in 2022, Daniel has been involved in a variety of projects ranging from a series of Ka-band BUCs to oscillator design, and more recently an internal case study launched to create a 5GHz ultra-low phase noise oscillator competing with the industry leaders. Daniel received his degree in Electronic Engineering from the University of York in 2022.



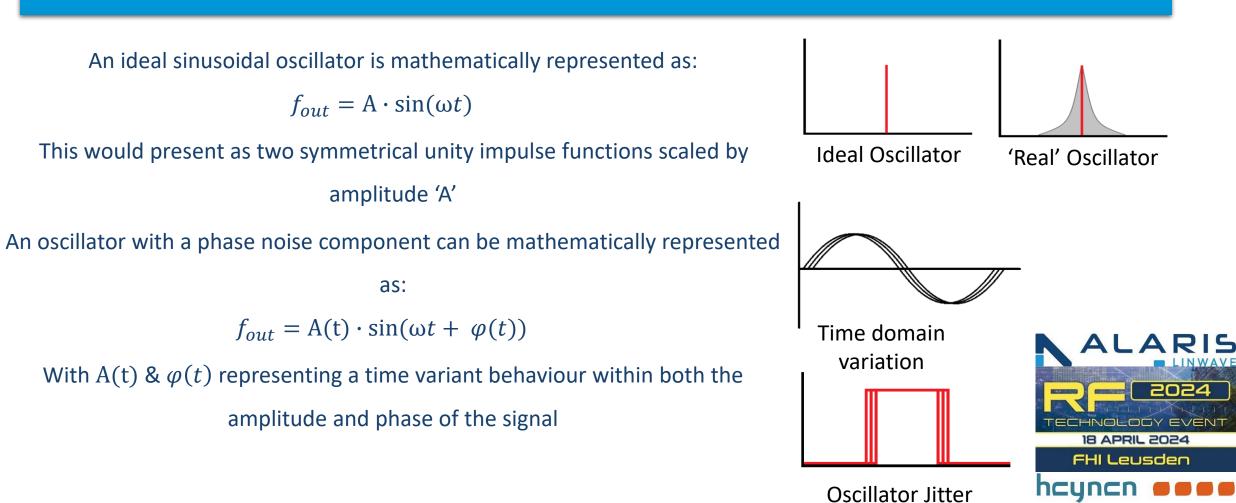
Agenda:

This presentation will outline the following:

- Outline Phase Noise
- Discuss Phase Noise within RADAR systems
- Oscillator theory
- High Frequency Oscillators



Phase Noise



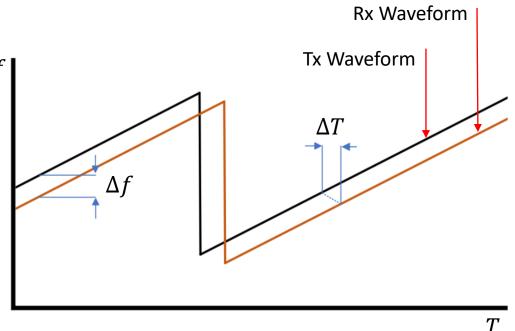
RADAR

RADAR works on principle by emitting a pulse of a known frequency & time and waiting for the echo to be received back. Using the deviation in

time, calculations can dictate the distance of the

object away from the RADAR transceiver.

By performing DSP of the deviation in signal, a spectrum of objects detected will be displayed.

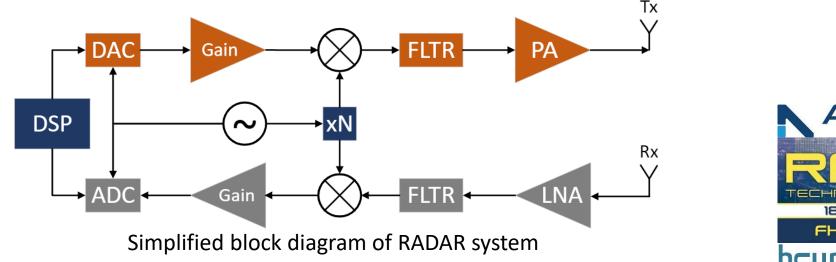


FMCW RADAR Waveform



To achieve resolution in object detection, the time duration of the pulse wants to be small, and hence the equivalent frequency is high. This means that the signal needs to be upconverted for generation, and downconverter for DSP after being received. Both stages involve mixing with an LO, this is known

as coherent RADAR providing both Tx & Rx share an LO.





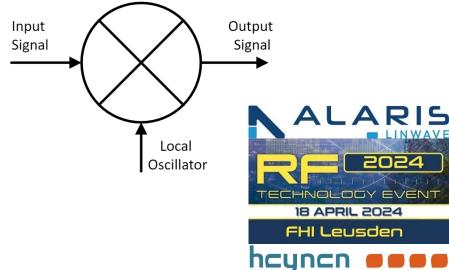
Phase Noise: Receiver Sensitivity

Receiver Sensitivity:

Receiver sensitivity is crucial in a RADAR system.

To allow for the RADAR pulse to be processed, and still collect data at a high enough resolution to distinguish the object, a process of frequency translation is often required.

The simplest form of RADAR frequency conversion is known as *heterodyne* conversion This involves a single stage conversion as shown below:

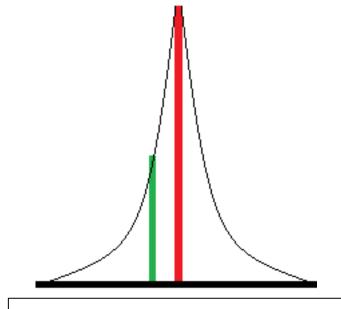


Phase Noise: Receiver Sensitivity

Given heterodyne systems only offer one stage of up/down conversion, the signal separation is limited to the bandwidth of the signal at baseband.

For example, a 50MHz BW baseband signal transmitted at 10GHz will require an LO of 9.95GHz.

If the LO phase noise is substantial in magnitude against the reflected signal of interest, it could be hidden under the noise profile.



Down-converted signal with 'wanted' (green) hidden by LO phase noise

Phase Noise: ADC Performance

Depending upon the frequency of sampling within the ADC, the clock reference phase noise can crucially affect the ability to sample accurately.

A decrease in SNR will increase the quantization error and reduce the effective number of sample bits

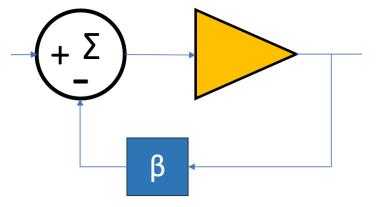
Clock jitter is given by:

$$T_{clk} = \frac{\sqrt{\int P(f)df}}{2\pi f_s}$$
ADC SNR is given by:

$$SNR = 20 \log_{10} \left(2\pi f_{in} \sqrt{(T_{clk})^2 + (T_{ap})^2} \right)$$

Crystal Oscillators

Crystal oscillators are comprised of a sliced quartz plate between two electrodes.



They offer high levels of phase noise performance, at the cost of increased complexity.

Crystal oscillators need careful design considerations to achieve good phase noise performance.

Methods Of Characterising Oscillators

Oscillators can be characterised and compared in a variety of ways, most commonly: **Phase Noise**: A metric of noise caused by random phase variations in the oscillator

Aging: The frequency drift of an oscillator with respect to time

Temperature Stability: The frequency drift of an oscillator with respect to temperature

Vibration Sensitivity: The shift of frequency given a source of shock/vibration



Oscillators – Temperature Variation

AT CUT:

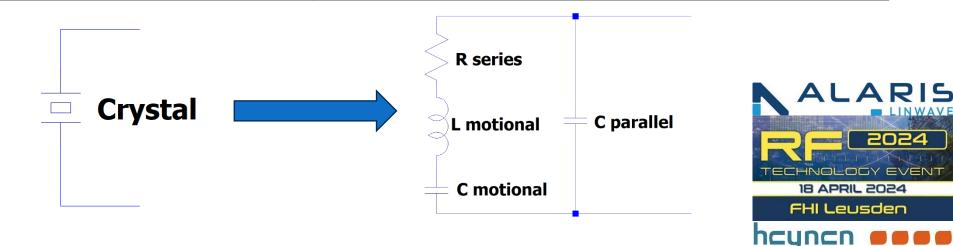
• Most used crystal cut

- 500kHz $\leq f_0 \leq$ 300MHz
- Temperature inflection point $\approx 25^{\circ}$ C

• Average phase noise & aging

SC CUT:

- 'Stress Compensated' crystal cut • $500 \text{kHz} \le f_0 \le 200 \text{MHz}$
- Temperature inflection point $\approx 85^{\circ}C$
 - Improved phase noise & aging



Oscillator Multiplication

Theoretical Phase Noise contribution from frequency multiplication:

$$P_N = 20 \cdot \log_{10}(N)$$

Where N represents the multiplication factor. For example, a 100MHz signal set for up conversion to 10GHz would report a phase noise degradation of:

 $P_N = 20 \cdot \log_{10}(100) = 40$ dB

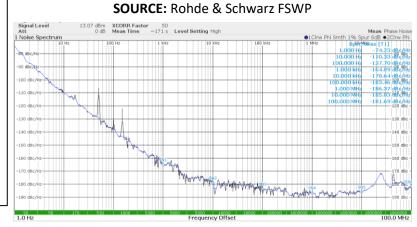
Custom Multipler Unit Multiple Multiple

How To Measure Phase Noise

Testing Process:

- Measures direct phase noise performance
- Can plot voltage
 against frequency for
 VCO
- Correlate the results





Cross-Correlation:

Cross correlation is a mathematical function comparing the similarities of two signals:

$$(f * g)(\tau) \triangleq \int_{-\infty} \overline{f(t-\tau)}g(t)dt$$

Phase Noise sensitivity is improved by: $Pn = 5 \log_{10}(Corr)$

Conclusion

To summarise, this presentation has included:

- Outline Phase Noise
- Discuss Phase Noise within RADAR systems
 - Oscillator theory
 - High Frequency Oscillators





Any Questions?





THANK YOU



Alaris Linwave: Ultra-Low Phase Noise Oscillators Come see us at Booth 11

